

ABSTRACT OF THE DISCLOSURE

~~The present~~Aspects of the invention is intended to~~can~~ provide an image data reducing device, a micro computer, and an electronic apparatus that can reduce image data without causing the increase in circuit scale and over spec.

~~[Means to Solve the Problem]~~

~~An~~The image data reducing device ~~100 comprises~~can include a reduced image data generating circuit ~~180~~ to which each component of YUV is input in series and that outputs YUV image data after reduced, and an output control signal generating circuit ~~190~~ that generates an output control signal 122 for controlling whether each component of YUV is output or not. The reduced image data generating circuit ~~180 includes~~can include a switching circuit that controls the presence of output for each component of image data input in series based on the output control signal ~~122~~. The output control signal generating circuit ~~190 includes~~can include a counting circuit 130 that counts input of Y component and resets the counted value when the counted value reaches the reciprocal number of reduction ratio, and a decimation pattern information storing unit ~~140~~ that stores decimation pattern information set in correlation with the counted value of the Y component. The output control signal generating circuit ~~190 generates~~can generate an output control signal based on the counted value of the Y component and the decimation pattern information.